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ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. **UTILITY** Patent Application

I.P.E.		PATENT DATE
SCANNED <i>gkky</i>	Q.A. <i>km</i>	

APPLICATION NO. 09/825279	CONT/PRIOR F	CLASS 324	SUBCLASS	ART UNIT 2858	2/36	EXAMINER MORIC Deady
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## APPLICANTS

Marcus Kuegler  
Ali Badiei

**TITLE**

### Method and apparatus for testing digital circuitry

PTO-2040  
12/99

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>  _____	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____	_____  _____  _____ (Primary Examiner) (Date)			<b>ISSUE FEE</b>  Amount Due Date Paid	
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	

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